CLAIM LISTING

No claims have been amended, canceled, withdrawn, or added. This claim listing is provided as a courtesy.

1.   (Previously Presented) An apparatus, comprising:

first circuit branch;

second circuit branch, coupled to the first circuit branch in a current mirror configuration;

an amplifier, coupled to the second circuit branch, comprising a scaled replica of the first branch and an amplifier output node; and

an enable node coupled to the first circuit branch, the second circuit branch, and the amplifier to indicate a first logical state at the amplifier output node if the first circuit branch is programmed and the second circuit branch is un-programmed and to indicate a second logical state at the amplifier output node if the first circuit branch is un-programmed and the second branch is programmed.

2.   (Previously Presented) The apparatus of claim 1, wherein the first, second, and third circuit branches include:

first, second, and third resistances, respectively, each having one terminal coupled to a first voltage;

first, second, and third current mirror devices, respectively, each having one terminal coupled to an opposite terminal of the first, second, and third resistances, respectively, the third current mirror device matching the first current mirror device, the first current mirror device coupled to the second current mirror device in a current mirror configuration; and

first, second, and third loads, respectively, each having one terminal coupled to an opposite terminal of the first, second, and third current mirror devices, respectively, a second terminal coupled to a second voltage, the third load matching the first load, and a third terminal coupled to the enable node.

3.   (Previously Presented) The apparatus of claim 2, further comprising a second branch output node coupled to the second branch and the first branch current mirror, a second branch output node potential to decrease if the second resistance increases relative to the first

resistance and to increase if the second resistance decreases relative to the first resistance, the amplifier output node potential being equivalent to the second branch output node potential if the second resistance is equivalent to the first resistance.

4.   (Previously Presented) The apparatus of claim 2, wherein the amplifier is to trip at a trip point associated with a predetermined amplifier input voltage, the trip point to sufficiently track a potential on the first current mirror device terminal coupled to the first resistance.

5.   (Previously Presented) The apparatus of claim 2, wherein the amplifier is to trip at a trip point associated with a predetermined amplifier input voltage, the trip point to sufficiently track a potential on the first current mirror device terminal coupled to the first resistance and to trip if the first resistance is approximately equal to the second resistance.

6.   (Previously Presented) The apparatus of claim 2, wherein the amplifier is to trip at a trip point associated with a predetermined amplifier input voltage, the trip point to sufficiently track a potential on the first current mirror device terminal coupled to the first resistance and to trip the amplifier if the potential on the first current mirror device terminal coupled to the first resistance is approximately equal to the second branch output node potential.

7.   (Previously Presented) The apparatus of claim 3, wherein an amplifier output node potential is equivalent to the second branch output node potential if the first resistance is approximately equal to the second resistance.

8.   (Original) The apparatus of claim 2, the first, second, and third resistances each further comprising a fuse element.

9.   (Previously Presented) The apparatus of claim 1, the first, second, and third current mirror devices each further comprising p-channel material.

Claims 10-20. (Canceled).

21.   (Original) An apparatus, comprising:

an amplifier having an output node; and

a gain stage, coupled to the amplifier, having a trip point to track a potential on the output node.

22.   (Original) The apparatus of claim 21, the amplifier comprising a sense branch coupled to a reference branch in a current mirror configuration.

23.   (Original) The apparatus of claim 22, the gain stage comprising a scaled replica of the reference branch or the sense branch.

24.   (Original) The apparatus of claim 22, further comprising first and second voltages each coupled to the amplifier and the gain stage.